

Refine Search

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L37 or L35	31

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L38

Refine Search

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Search History

DATE: Tuesday, January 20, 2004 [Printable Copy](#) [Create Case](#)

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<i>DB=PGPB,USPT,USOC,EPAB,JPAB,DWPI,TDBD; PLUR=YES; OP=ADJ</i>			
<u>L38</u>	L37 or l35	31	<u>L38</u>
<u>L37</u>	l26 and L36	17	<u>L37</u>
<u>L36</u>	l1 or l2 or l4	34	<u>L36</u>
<u>L35</u>	L34 or l11 or l10 or l6	31	<u>L35</u>
<u>L34</u>	L33 or l31	31	<u>L34</u>
<u>L33</u>	l26 and L32	11	<u>L33</u>
<u>L32</u>	l18 or l16 or l13 or l12 or l5 or l4	35	<u>L32</u>
<u>L31</u>	l30 or l29 or l28 or l27 or l25 or l24 or l22 or l21 or l20 or l19	29	<u>L31</u>
<u>L30</u>	l23 and l8	1	<u>L30</u>
<u>L29</u>	L26 and l5	9	<u>L29</u>
<u>L28</u>	L26 and l1	9	<u>L28</u>
<u>L27</u>	L26 and l3	8	<u>L27</u>
<u>L26</u>	714/\$.ccls.	43418	<u>L26</u>

<u>L25</u>	L23 and l4	1	<u>L25</u>
<u>L24</u>	L23 and l2	1	<u>L24</u>
<u>L23</u>	identify\$5 near3 (instruction\$1 near2 sequence\$1)	189	<u>L23</u>
<u>L22</u>	re-execut\$5 near4 (memory access request\$1)	3	<u>L22</u>
<u>L21</u>	(updat\$5 near3 pointer\$1) same (memory access error\$1)	1	<u>L21</u>
<u>L20</u>	machine check abort handle\$	4	<u>L20</u>
<u>L19</u>	(fake data) same (memory access error\$1)	1	<u>L19</u>
<u>L18</u>	((memory access\$5) near3 error\$1) same (instruction\$1 near3 sequence\$1)	7	<u>L18</u>
<u>L17</u>	L15 and l14	14	<u>L17</u>
<u>L16</u>	L15 same l14	1	<u>L16</u>
<u>L15</u>	recover\$5 near4 (memory near3 error\$1)	354	<u>L15</u>
<u>L14</u>	checkpoint\$5	5360	<u>L14</u>
<u>L13</u>	L12	13	<u>L13</u>
<u>L12</u>	(exception\$1) near4 ((memory access\$5) near3 error\$1)	13	<u>L12</u>
<u>L11</u>	(rais\$5 near3 exception\$1) near4 ((memory access\$5) near3 error\$1)	1	<u>L11</u>
<u>L10</u>	L9 and l4	2	<u>L10</u>
<u>L9</u>	execut\$5 near4 (instruction\$1 near3 sequence\$1)	10890	<u>L9</u>
<u>L8</u>	log\$5 near4 (memory near3 error\$1)	731	<u>L8</u>
<u>L7</u>	log\$5 near4 (emory near3 error\$1)	0	<u>L7</u>
<u>L6</u>	(poll\$5 near3 register\$1) same ((memory access\$5) near4 error\$1)	2	<u>L6</u>
<u>L5</u>	L4	18	<u>L5</u>
<u>L4</u>	log\$6 near3 ((memory access\$5) near3 error\$1)	18	<u>L4</u>
<u>L3</u>	((memory access\$5) near3 error\$1) near5 register\$1	35	<u>L3</u>
<u>L2</u>	monitor\$5 near3 ((memory access\$5) near3 error\$1)	13	<u>L2</u>
<u>L1</u>	monitor\$5 near3 (memory near3 (access\$5 near3 error\$1))	16	<u>L1</u>

END OF SEARCH HISTORY